

* details on how the software works →
what happens on the system.

* more figures! a figure that
~~captures~~ captures the flow of
the whole ~~pro~~ process.

method for:

- CPU
- FPGA
- GPU
- Rack
- Datacenter

* measuring & knowing the power cons.
is important to creating the most
~~eff~~ efficient design.

* CPUs ^{all silicon} should provide an interface
that allow us to put CPUs / silicon into
modes that exercise CPU at max power,
disabling ~~power~~ power optimizations.
Same for all silicon like memory, I/O
controllers, bus controllers.

- * applicable to lots of computing platforms
- * examples of things, or a meaningful defined phrase for ultra-efficient, rather than just marketing claims.